



**INDIAN INSTITUTE OF INFORMATION AND TECHNOLOGY UNA  
HIMACHAL PRADESH**  
(An institute of national importance under MoE)  
**Saloh, Una- 177209**

[www.iiituna.ac.in](http://www.iiituna.ac.in)

IIITU/Acad./AY-2025-26/Even Sem-PhD-Admission/2025-**1185**

28, Nov.'25

**CALL FOR APPLICATIONS FOR FULL-TIME PHD PROGRAM**

The Indian Institute of Information Technology Una (IIIT Una) is recognized as an Institution of National Importance under the Ministry of Education, Government of India, established by an Act of Parliament in the academic year 2014-15. It currently offers four undergraduate programs—B.Tech. in Computer Science & Engineering (CSE), Electronics Communication & Engineering (ECE), CSE (Cyber Security), and CSE (Data Science) with a total sanctioned intake of 294 for the academic year 2025-26. Additionally, IIIT Una provides two postgraduate M.Tech. programs: one in the School of Computing specializing in Data Science/Cyber Security with sanctioned strength of 30, and the other in the School of Electronics specializing in Microelectronics and VLSI Design with a sanctioned strength of 20.

Applications for full-time PhD admission at Indian Institute of Information Technology Una (IIIT Una) for the even semester of academic year 2025-26 under the Non-Stipendiary category are invited from motivated students with excellent consistent academic records from secondary school through post-graduation in the following disciplines.

School	Number of Seats	Category
School of Computing	Full time-08	Non-Stipendiary
School of Electronics	Full time-07	

\*The reservation rules will be followed as per Government of India guidelines prevailing at the time of admission.

**Eligibility Criteria: The eligibility criteria for the full-time Ph.D. programs shall be as per the Institute's Ph.D. Ordinance.**

For the Minimum Eligibility Qualification (MEQ) for admission to Regular Full-time PhD program refer Table 1 below:

**Table 1:** Minimum Eligibility Qualification for admission to Regular Full-time PhD Program.

Sr. No.	Qualifying Degree	Minimum Performance in Qualifying Degree for General/ OBC (Non Creamy Layer)/ EWS Category Candidate	Minimum Performance in Qualifying Degree for SC/ST/PwD Category Candidate	Qualification Through National Level Examination Requirement
1.	M.Tech./ M.E.	60% marks or 6.00 CGPA on a 10-point scale	55% marks or 5.5 CGPA on a 10-point scale	Nil

2.	B.Tech./ B.E program	60% marks or 6.00 CGPA on a 10-point scale	55% marks or 5.5 CGPA on a 10-point scale	Qualified GATE/CSIR/UGC NET/DST INSPIRE/JEST/N BHM or other national fellowship
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#### **Exemptions:**

The requirement of qualification in GATE / National level Exam is waived off for the following categories of applicants:

Candidates from Centrally Funded Technical Institutes (CFTIs) completed B.Tech./B.E./Integrated M.Tech./ Integrated M.Sc. program (or any other program of minimum four-year duration, admission to which is on the basis of JEE), must obtain a CGPA of 7.5 or above (on a 10-point scale) at the time of graduation, and before they formally register for the PhD program (75% aggregate marks, if marks is the primary mode of evaluation).

#### **III. Schedule of Selection Process:**

i)	Release of advertisement on website	28, Nov.'25
ii)	Last date for receipt of Applications (through online mode only)	28, Dec.'25
iv)	Scrutiny of applications and shortlisting of candidates for interview	05, Jan.'25
v)	Tentative Date of Written Test and Interview	12, Jan.'25
vi)	Tentative Date of Result Declaration	19, Jan.'25
vii)	Fee payment/Registration	26, Jan'25 – 30, Jan'25
ix)	Commencement of Classes	02, Feb'25

The interested candidates may register their candidature by Submitting the online application with scanned copies of certificates, through the following google link:

<https://forms.gle/vzLrVm848EHXWa2m7>

#### **General terms and conditions:**

- The written examination will be of 50 marks (50 multiple choice questions of 01 marks).
- The syllabus of the written examination is GATE 2025 Syllabus (School of Computing: GATE Syllabus for Computer Science Engineering (CSE) and Electronics and Communication Engineering (ECE) 2025 for School of Computing).
- The applicants who are GATE/NET Qualified may be exempted from the written test.
- Original documents of age proof/degrees/mark sheets/experience must be presented at the time of Written Test and Interview.
- Applicants in employment (private, government or any other organization) are required to submit a “No Objection Certificate” from the employer at the time of interview.
- No TA/DA will be paid for attending the interview.

- Canvassing in any form will render a candidate ineligible.
- Institute reserves the right to select or reject a candidate without assigning any reason/s thereof.
- The institute reserves the right to defer or cancel the advertisement at any stage of processing without assigning any reasons, if required.
- The institute reserves the right to place a reasonable limit on the total number of candidates to be called for interview. Mere fulfilment of qualification does not entitle a candidate to be called for interview.
- No stipend will be paid by the institute for the selected candidates. However, the PhD scholars of the institute are encouraged to apply for various fellowships available from GoI and other agencies.
- The applicant if shortlisted for the interview should submit the choice of the faculty member (regular) as a PhD Supervisor in the sealed envelope before the interview only.



4/12/23

REGISTRAR